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(71) Applicant (for all designated States except US): **TOYOTA JIDOSHA KABUSHIKI KAISHA [JP/JP]; 1, Toyota-cho, Toyota-shi, Aichi, 4718571 (JP).**

(72) Inventor; and

(75) Inventor/Applicant (for US only): **NISHIWAKI, Kat-suhiko [JP/JP]; c/o TOYOTA JIDOSHA KABUSHIKI KAISHA, 1, Toyota-cho, Toyota-shi, Aichi, 4718571 (JP).**

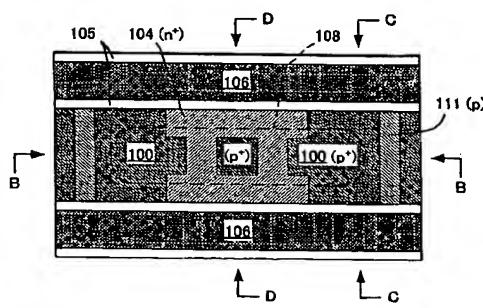
(74) Agents: **OKADO, Akiyoshi et al.; Nagoya Center Building, Annex 2nd Floor, 2-22, Nishiki 2-chome, Naka-ku, Nagoya-shi, Aichi, 4600003 (JP).**

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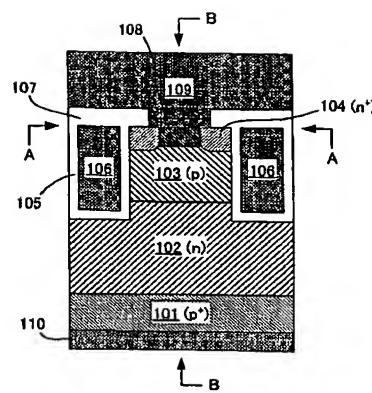
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[Continued on next page]

(54) Title: SEMICONDUCTOR DEVICE



(57) Abstract: To present a semiconductor device capable of operating stably even at large current, by lessening current concentration into the corners of contact opening after switching off and suppressing local heat generation without raising the ON voltage. In an insulated gate transistor divided by P field region 111 and gate electrode 106, having N+ emitter region 104 and P+ emitter region 100, and controlling conduction between emitter and collector by voltage applied to gate electrode 106, the shape of contact opening 108 contacting emitter (N+ emitter region 104 and P+ emitter region 100) and emitter electrode is formed of curved lines at four corners. Hence, eliminating right-angle apex, hole current from the field region into the emitter electrode after switching off is prevented from concentrating at one point.





SK, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ,
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